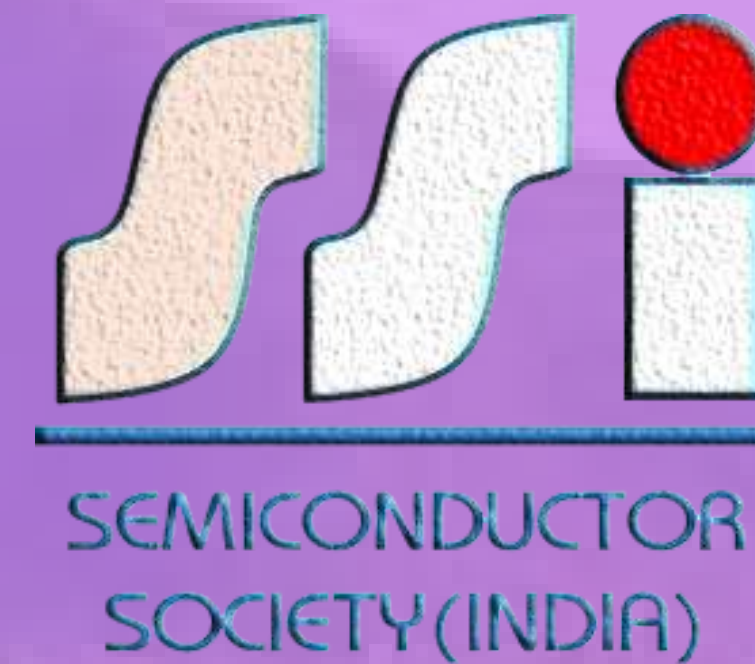
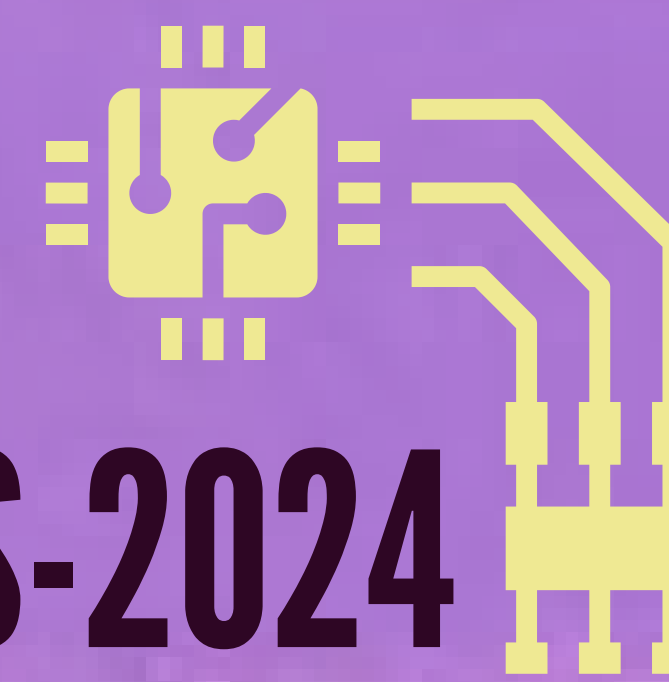


INTERNATIONAL SYMPOSIUM ON

SEMICONDUCTOR MATERIALS AND DEVICES-2024



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ORGANISED BY
DEPARTMENT OF PHYSICS,
UNIVERSITY OF KASHMIR
&
SEMICONDUCTOR SOCIETY (INDIA)
IN COOPERATION WITH
SOCIETY FOR
SEMICONDUCTOR DEVICES
&
ISLAMIC UNIVERSITY OF SCIENCE & TECHNOLOGY

SEPTEMBER. 4-6, 2024

VENUE:



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SCOPE OF CONFERENCE

We are delighted to invite esteemed researchers, scholars, and professionals worldwide to the "International Symposium on Semiconductor Materials and Devices-2024" at the University of Kashmir, Srinagar, India. Jointly organized by the Department of Physics, University of Kashmir, and the Semiconductor Society of India (SSI), our symposium provides a crucial platform for collaboration and exchange in the field. Featuring recent advances, breakthroughs, and emerging trends, the event promises engaging discussions, insightful presentations, and unparalleled networking opportunities. Attendees will interact with experts, explore innovative concepts, and contribute to semiconductor science and technology. Together, let's shape the future of semiconductor materials and devices.

ABOUT SEMICONDUCTOR SOCIETY (INDIA)

The Semiconductor Society (India) that is SSI, a leading non-profit organization, has been nurturing the Semiconductor Community for over four decades, boasting a membership of over seven hundred. Our mission is to foster collaboration and knowledge exchange among scientists and technologists specializing in Semiconductor Materials and Devices. Through lectures, seminars, invited talks and conferences, we facilitate dynamic interactions and promote cutting-edge advancements in semiconductor technologies. Our vibrant forum cultivates a culture of continuous learning and exploration, providing opportunities to share insights, innovations, and best practices. Join our inclusive ecosystem, where expertise meets curiosity, and seize the endless possibilities that lie ahead in navigating the ever-evolving landscape of technology.

ABOUT UNIVERSITY OF KASHMIR

Established in 1948, the University of Jammu and Kashmir was later divided into two institutions in 1969: the University of Kashmir in Srinagar and the University of Jammu in Jammu. Located in Hazratbal, Srinagar, the University of Kashmir enjoys a picturesque setting flanked by Dal Lake to the east and Nigeen Lake to the west. Its 247-acre main campus comprises Hazratbal, Naseem Bagh, and Mirza Bagh sections, with expansion plans at Zakura. With satellite campuses in Anantnag, Baramulla, and Kupwara, and a sub-office in Jammu, the university aims to enhance higher education accessibility across the Kashmir valley. Offering diverse programs across faculties, the university fosters an intellectually stimulating environment for learning and research. Recognized for its excellence, it has received Grade-A+ accreditation from the National Assessment & Accreditation Council (NAAC) of India, underscoring its dedication to teaching and research quality.

TOPICS TO BE COVERED

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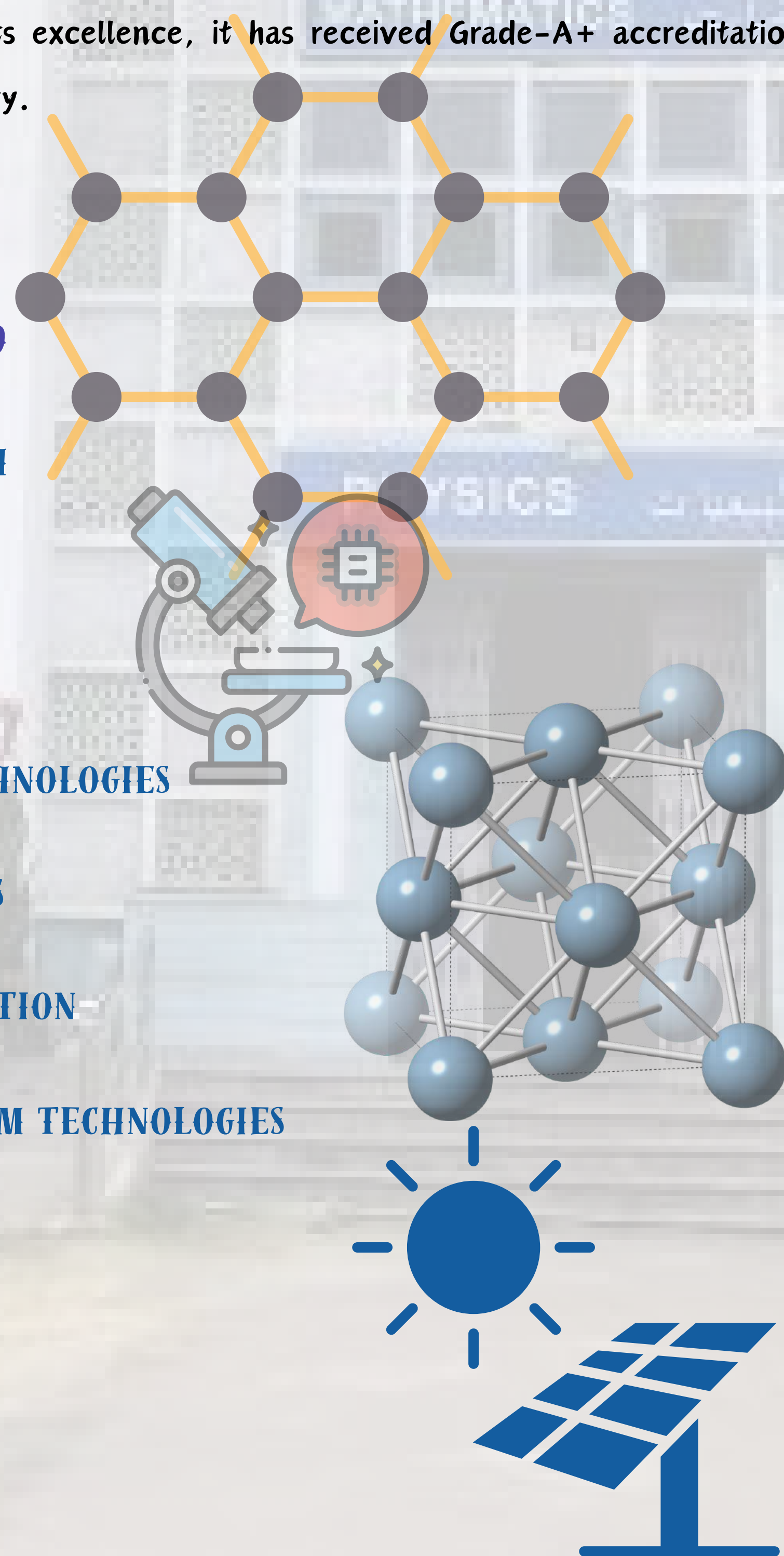
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CALENDAR AND DEADLINE

Abstract Submission deadline: 30th June 2024

Acceptance of Abstracts: 15th July 2024

Pre Registration:

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